

United States Patent and Trademark Office



UNITED STATES DEPARTMENT OF COMMERCE United States Patent and Trademark Office Address: COMMISSIONER FOR PATENTS P.O. Box 1450 Alexandria, Virginia 22313-1450 www.uspto.gov

DATE MAILED: 06/19/2006

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO
09/683,733	02/07/2002	Donald G. Aday	BUR920010167	1401
23550	7590 06/19/2006		EXAMINER	
HOFFMAN WARNICK & D'ALESSANDRO, LLC			NELSON, FREDA ANN	
75 STATE S'			ART UNIT	PAPER NUMBER
ALBANY, N			3639	

Please find below and/or attached an Office communication concerning this application or proceeding.

	Application No.	Applicant(s)				
Office Antinu Occurrence	09/683,733	ADAY ET AL.				
Office Action Summary	Examiner	Art Unit				
	Freda A. Nelson	3639				
The MAILING DATE of this communication app Period for Reply	ears on the cover sheet with the c	orrespondence address				
A SHORTENED STATUTORY PERIOD FOR REPLY WHICHEVER IS LONGER, FROM THE MAILING DA - Extensions of time may be available under the provisions of 37 CFR 1.13 after SIX (6) MONTHS from the mailing date of this communication. - If NO period for reply is specified above, the maximum statutory period w - Failure to reply within the set or extended period for reply will, by statute, Any reply received by the Office later than three months after the mailing earned patent term adjustment. See 37 CFR 1.704(b).	ATE OF THIS COMMUNICATION 16(a). In no event, however, may a reply be tim ill apply and will expire SIX (6) MONTHS from cause the application to become ABANDONE	N. nety filed the mailing date of this communication. D (35 U.S.C. § 133).				
Status						
1) Responsive to communication(s) filed on 07 Fe	ebruary 2002					
·						
_ ,	Since this application is in condition for allowance except for formal matters, prosecution as to the merits is					
closed in accordance with the practice under E	-					
Disposition of Claims						
4)⊠ Claim(s) <u>1-26</u> is/are pending in the application.						
4a) Of the above claim(s) is/are withdrawn from consideration.						
5) Claim(s) is/are allowed.						
6)⊠ Claim(s) <u>1,3-4, 10,17 and 20</u> is/are rejected.						
7) Claim(s) <u>7,3-4, 70,17 and 20</u> is are rejected. 7) Claim(s) <u>2-9,11-16,18,19 and 21-26</u> is/are objected to.						
8) Claim(s) are subject to restriction and/or						
, ————————————————————————————————————	4					
Application Papers						
9) The specification is objected to by the Examine						
10) ☐ The drawing(s) filed on is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.						
Applicant may not request that any objection to the						
Replacement drawing sheet(s) including the correct						
11)☐ The oath or declaration is objected to by the Ex	aminer. Note the attached Office	Action or form PTO-152.				
Priority under 35 U.S.C. § 119						
12) ☐ Acknowledgment is made of a claim for foreign a) ☐ All b) ☐ Some * c) ☐ None of:)-(d) or (f).				
1. Certified copies of the priority documents						
2. Certified copies of the priority documents						
3. Copies of the certified copies of the prior		ed in this National Stage				
application from the International Bureau						
* See the attached detailed Office action for a list of the certified copies not received.						
Attachment(s)						
1) X Notice of References Cited (PTO-892)	4) Interview Summary					
2) Notice of Draftsperson's Patent Drawing Review (PTO-948) 3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)	Paper No(s)/Mail D 5) Notice of Informal F					
Paper No(s)/Mail Date	6)					

Art Unit: 3639

DETAILED ACTION

This is in response to a letter for a patent filed February 7, 2002 in which claims 1–26 were presented for examination. Claims 1-26 are pending.

Information Disclosure Statement

1. The information disclosure statement (IDS) submitted on 02/25/2002 is in compliance with the provisions of 37 CFR 1.97. Accordingly, the information disclosure statement is being considered by the examiner. A copy of PTO-1449 is attached hereto.

Claim Rejections - 35 USC § 112

The following is a quotation of the second paragraph of 35 U.S.C. 112:

The specification shall conclude with one or more claims particularly pointing out and distinctly claiming the subject matter which the applicant regards as his invention.

2. Claims 1 and 3-4 are rejected under 35 U.S.C. 112, second paragraph, as being incomplete for omitting essential steps, such omission amounting to a gap between the steps. See MPEP § 2172.01. The omitted steps are:

"designating base parameters" and "designating custom parameters".

The applicant has recited the claim language "providing an interface for designating base parameters", but has nowhere recited that the base parameters are actually designated before selecting a base die. The examiner believes the designating step is critical or essential to the practice of the invention.

Art Unit: 3639

Claim 3 is rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention.

Claim 3 recites the limitation "the designated chip" in line 2. There is insufficient antecedent basis for this limitation in the claim.

Claim 4 recites the limitation "the designated base parameters" in line 2. There is insufficient antecedent basis for this limitation in the claim.

Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

- (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 3. Claims 1, 10, 17, and 20 are rejected under 35 U.S.C. 103(a) as being unpatentable over Nanbu et al. (US Patent Number 6,970,754).

As for claims 1, 10, 17, and 20, Nanbu et al. disclose a computerized method of calculating a custom die price, comprising:

providing an interface for designating base parameters (col. 4, lines 22-24); selecting a base die based on the base parameters (col. 14, lines 25-33); creating a reference die based on a design methodology and the base parameters(col. 4, lines 49-67);

Art Unit: 3639

calculating a complexity factor based on designated custom parameters and the reference die (col. 8, line 54-col. 9, line 10); and

calculating the custom die price based on the base die, the custom parameters and the complexity factor (FIG. 41).

Allowable Subject Matter

- 4. Claims 3-4 would be allowable if rewritten or amended to overcome the rejection(s) under 35 U.S.C. 112, 2nd paragraph, set forth in this Office action.
- 5. Claims 2-16, 18-19, and 21-26 objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

Conclusion

- 6. The examiner has cited prior art of interest, for example:
- 1) Liu (US PG Pub. 2003/0125825), which discloses a computer-aided progressive die design system and method.
- 2) Guest et al. (Patent Number 6,252,981), which disclose a system and method for selection of a reference die.
- 3) Magee et al. (US Patent Number 6,529,790), which disclose a computation of die-per-wafer considering production technology and wafer size.
- 4) Ladd, Steven, "Advanced MCM design", August 1995, Printed Circuit Design, v12n8, PP: 10-16, 7 pgs.

Application/Control Number: 09/683,733 Page 5

Art Unit: 3639

5) Kuo et al., "An Overview of Manufacturing Yield and Reliability Modeling for Semiconductor Products", August 1999, Proceedings of the IEEE, VOL. 87, NO. 8. pp. 1329-1344.

7. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Freda A. Nelson whose telephone number is (571) 272-7076. The examiner can normally be reached on Monday - Friday.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, John Hayes can be reached on 571-272-6708. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

FAN 06/08/2006

SUPERVISORY PATENT EXAMINER